

FIG. 1

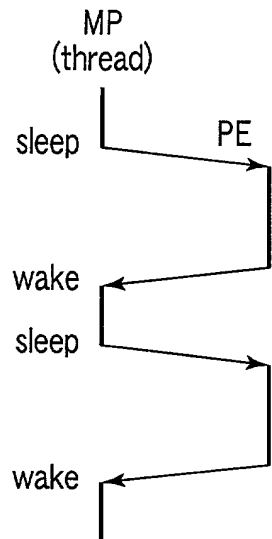


FIG. 2A

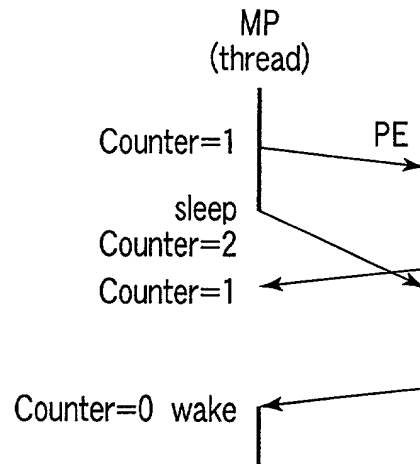


FIG. 2B

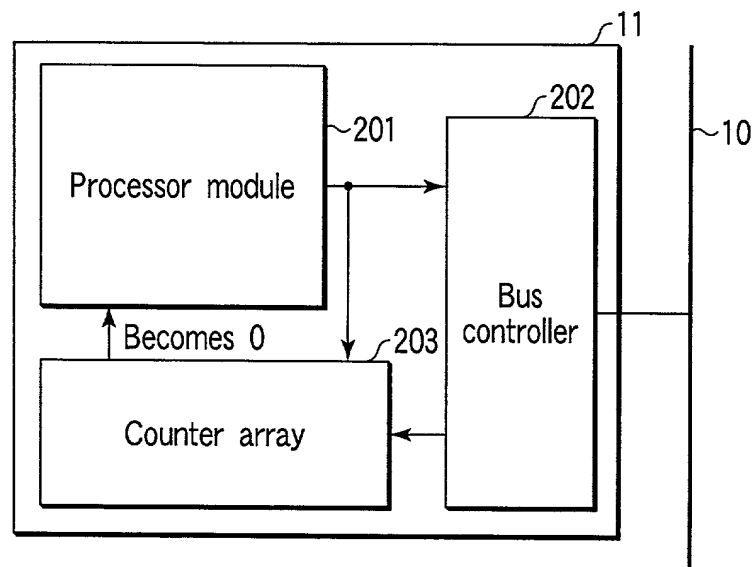


FIG. 3

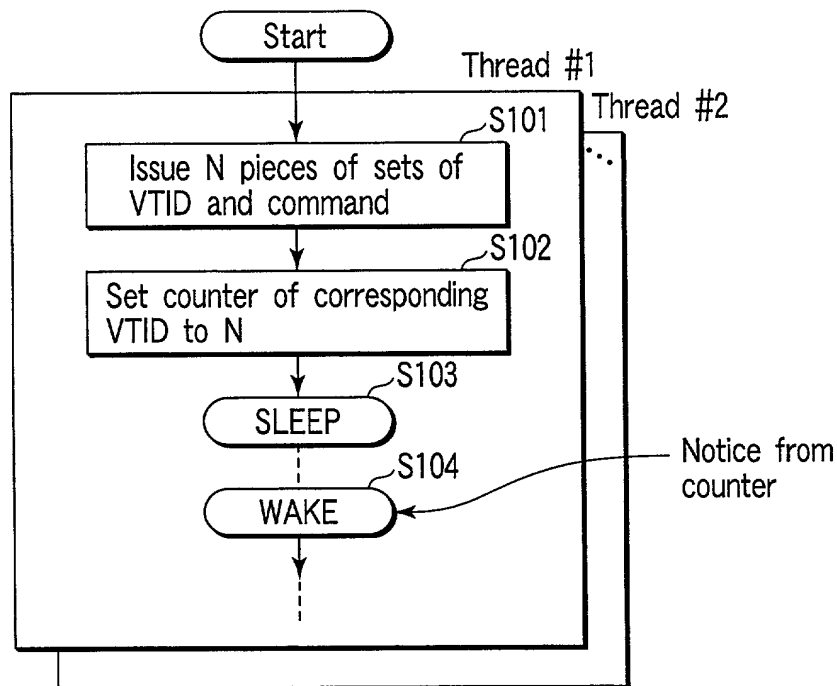


FIG. 4

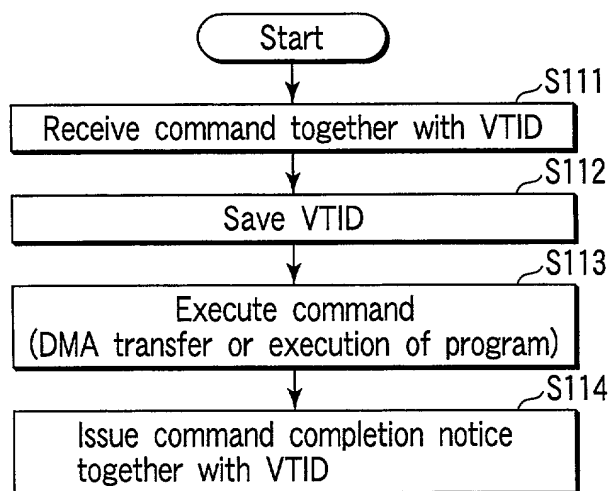


FIG. 5

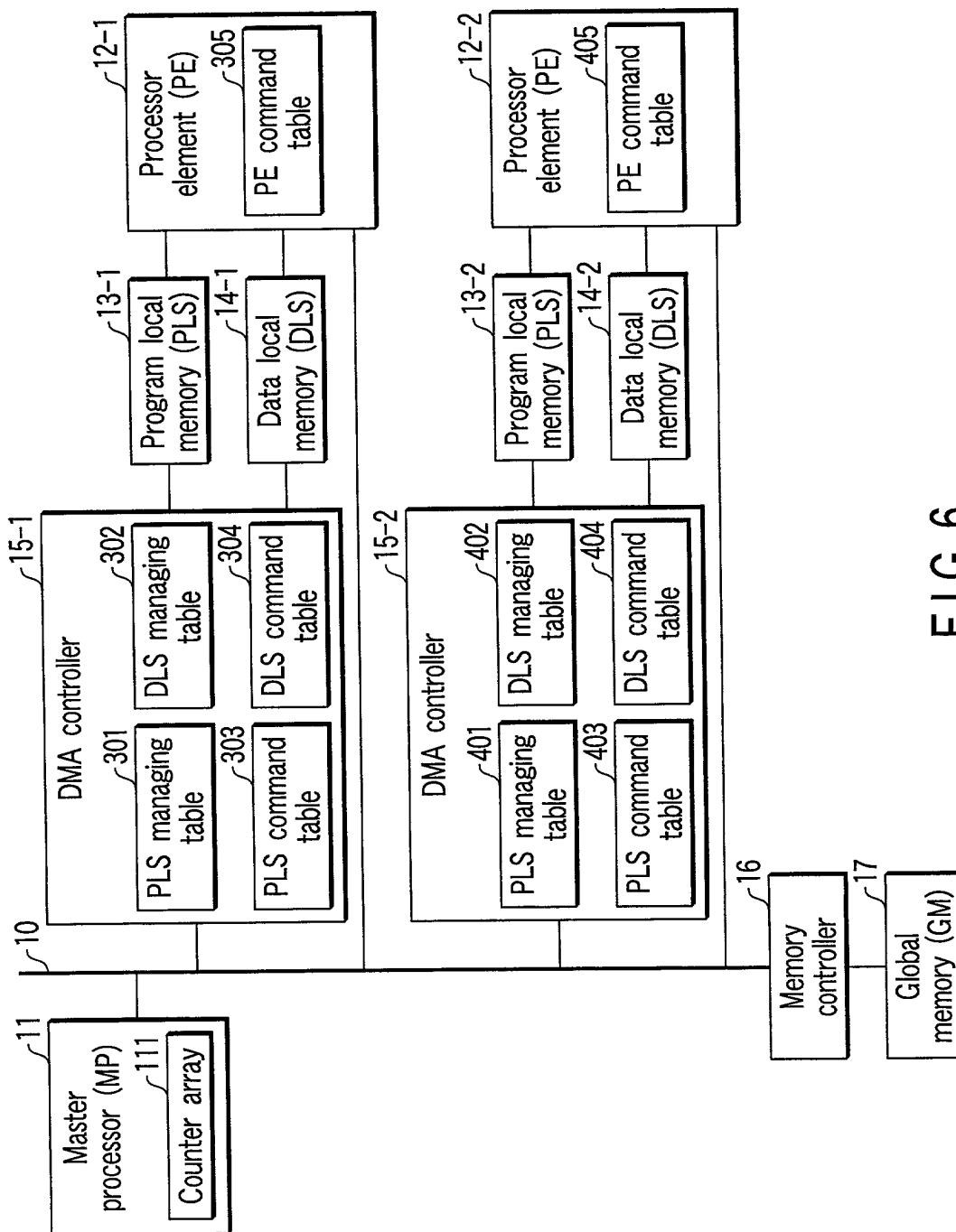


FIG. 6

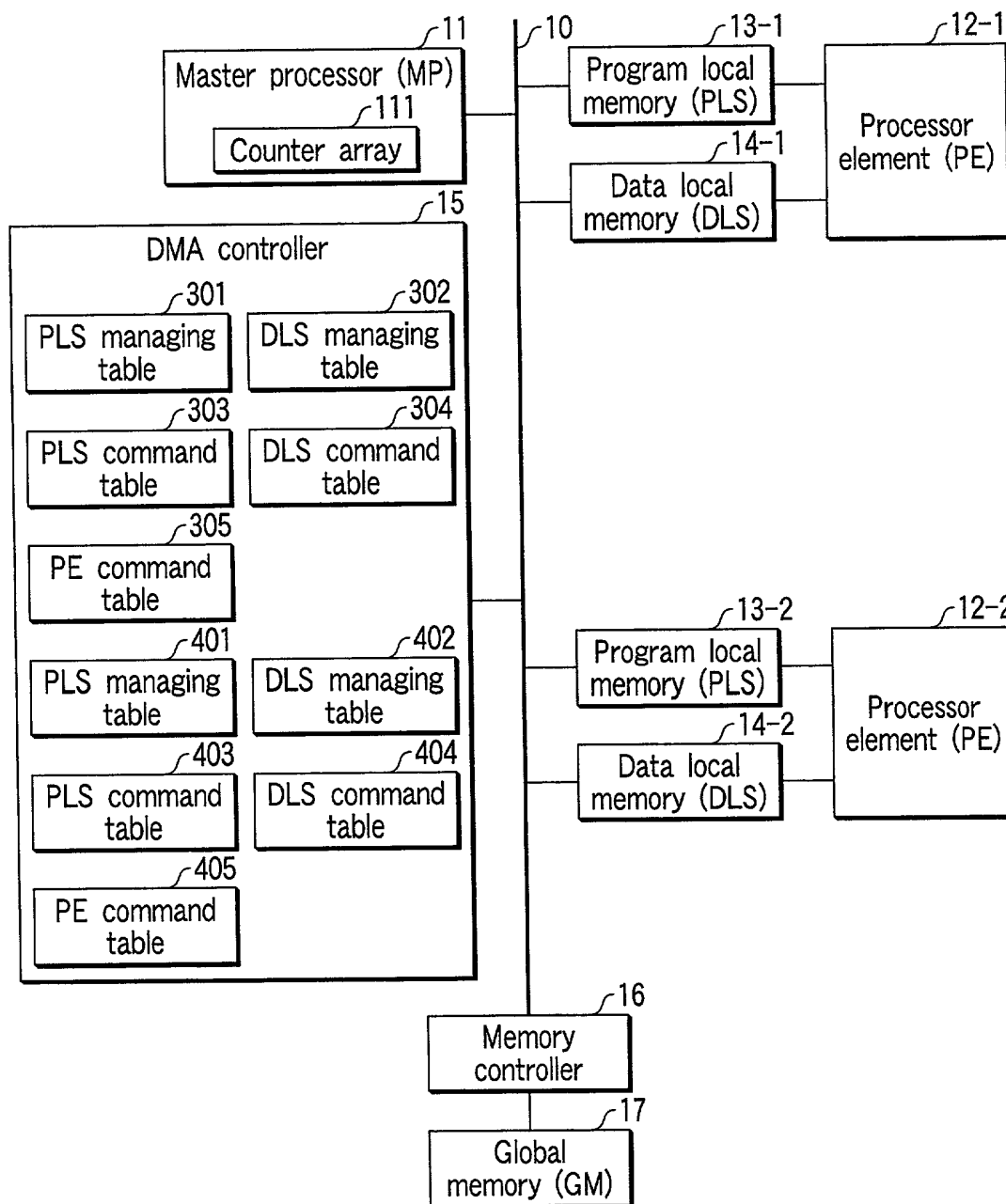


FIG. 7

PLS managing table

0	44
1	44
2	44
3	-
<hr/>	
12	-
13	-
14	-
15	-

FIG. 8

DLS managing table

0	44	D	
1	44	D	
2	44		R
3	44		R
<hr/>			
12	-		
13	-		
14	-		
15	-		

FIG. 9A

DLS' managing table

0	44	D	
1	44	D	
2	44	D	R
3	44	D	R
<hr/>			
12	-		
13	-		
14	-		
15	-		

FIG. 9B

VTID	GM address of origin of transfer	Forwarding page (bit vector)							
		0	1	2	3		13	14	15
44	0x120000	1	1	1	0	0	0	0
50	0x140000	0	0	0	0	0	1	1

PLS command table

FIG. 10

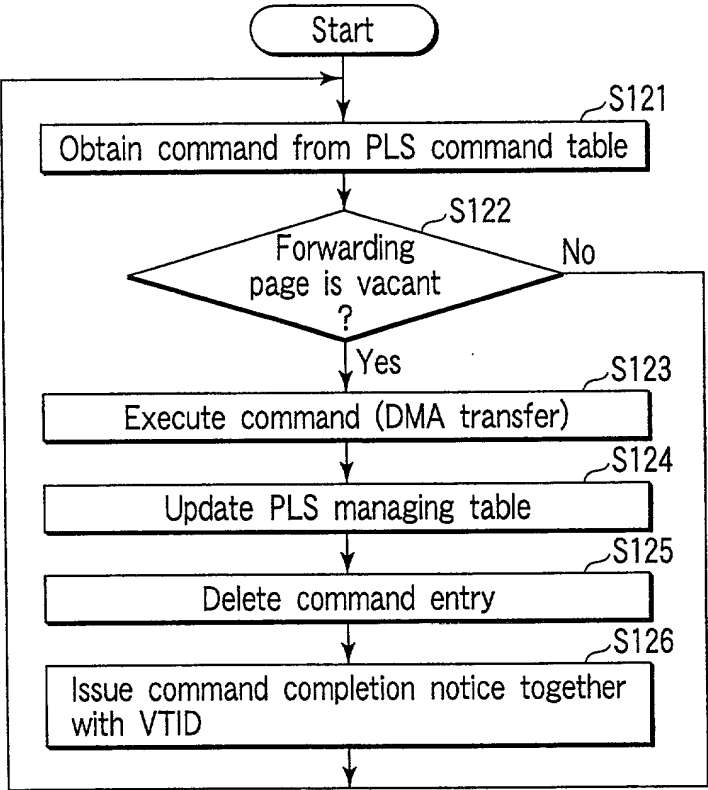


FIG. 11

VTID	GM address	Direction	Transfer page (for each page)									
			0	1	2	3	13	14	15		
44	0x20000	D	D	D	R	R	0	0	0		
44	0x40000	G	0	0	D	D	0	0	0		

Constitution of DLS command table

FIG. 12

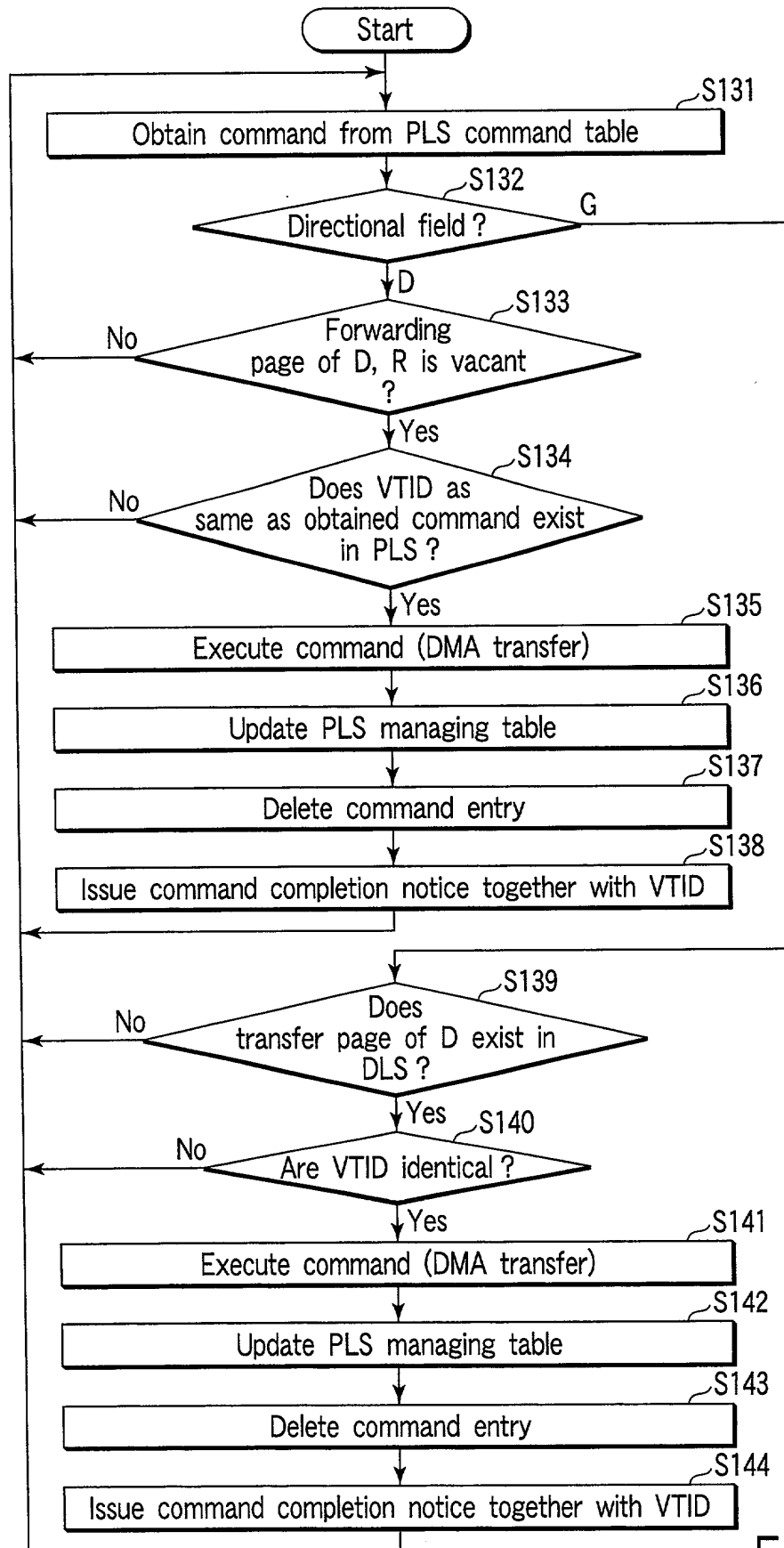


FIG. 13

VTID	Start page	Used page (bit vector)									
		0	1	2	3	13	14	15		
44	0	1	1	0	0	0	0	0		
50	0	0	0	1	1	0	0	0		

PE command table

FIG. 14

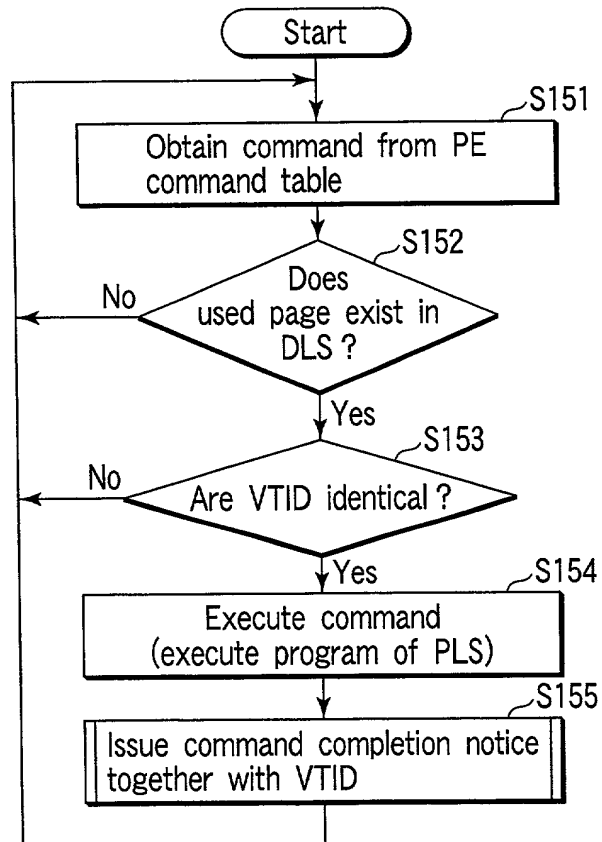


FIG. 15

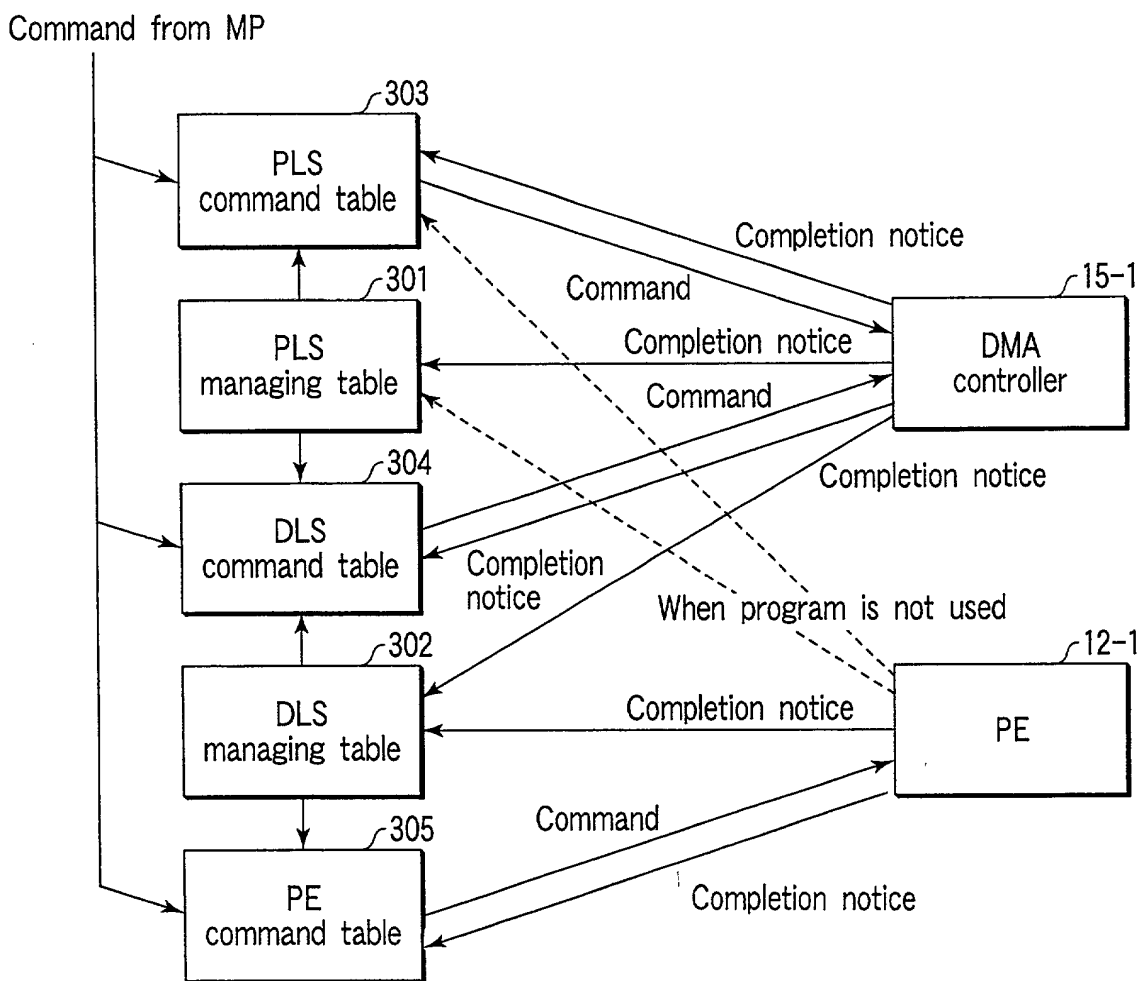


FIG. 16

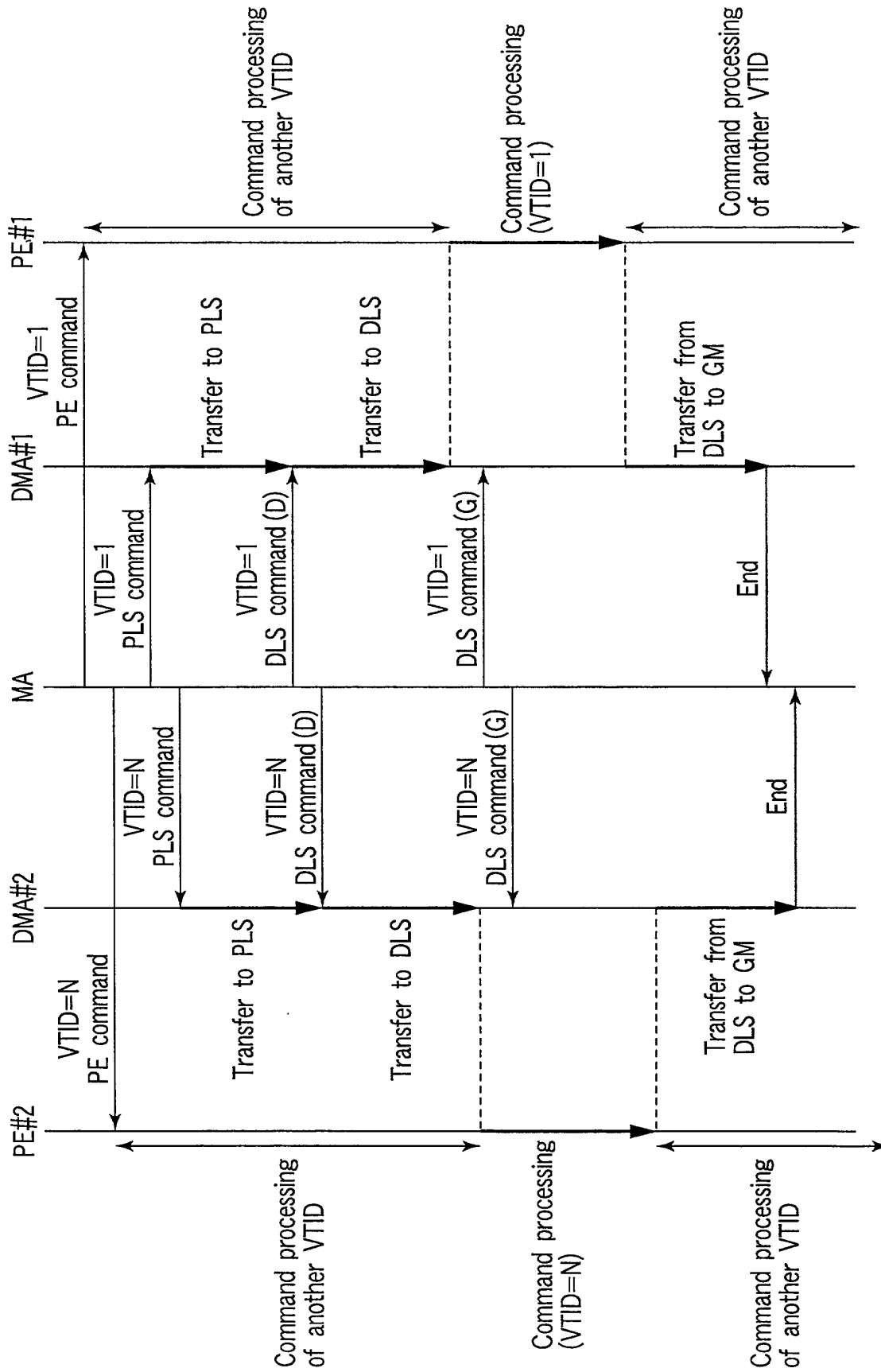


FIG.17